



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 2138  
Examiner: Ms. Cynthia H. Britt

In re PATENT APPLICATION of:

Applicant : I-Ming LIN et al.

Serial No. : 10/614,040

Filed : July 8, 2003

For : SIGNAL TESTING OF INTEGRATED  
CIRCUIT CHIP

Attorney Ref. : TOP 296

AMENDMENT AFTER  
FINAL REJECTION

May 25, 2007

**Mail Stop: AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is responsive to the Office Action of March 28, 2007, the period for reply to which has been set to expire on June 28, 2007.

A fee of \$None is being submitted concurrently. Should this remittance be accidentally missing, however, or should any additional fees be needed (including additional extension of time fees, since Applicants hereby provisionally petition for any extensions that may be deemed necessary to avoid abandonment), the Director may charge such fees to our Deposit Account number 18-0002.

Please amend the above-identified application as specified on the following pages, and then reconsider the application in view of the Remarks that are presented thereafter.